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IN THE CLAIMS

Please cancel claims 18-29 without prejudice and amend claims 1, 2, 7, 9, 10, 13 and 15 as follows:

1. (Amended) A programmable element, comprising:

a first device on a substrate having a first electrode and a first insulator disposed between the substrate and said first electrode, said first insulator having a first value of a given parameter;

a second device on a substrate having a second electrode and a second insulator disposed between the substrate and said second electrode wherein said second insulator thickness is greater than the first insulator, and

said second insulator having a second value of said given parameter that is different from said first value;

A1 wherein said first and second electrodes of said first and second devices are coupled to one another on a continuous layer; and

a source of programming energy coupled to said first device and causing it to permanently decrease in resistivity,

wherein a programmed state of said first device is indicated by a conductive state of said second device.

2. (Amended) The programmable element of claim 1, wherein said given parameter is selected from the group consisting of density, ~~thickness~~, and insulative value.

3. (Original) The programmabl element of claim 2, wherein said first insulator has a dielectric breakdown voltage that is less than that of said second insulator.

4. (Original) The programmable element of claim 3, wherein said first insulator is selected from the group consisting of silicon oxide, silicon nitride, silicon oxynitride, or combinations of two or more of silicon oxide, silicon nitride, and silicon oxynitride.

5. (Original) The programmable element of claim 3, wherein said first device further comprises a third electrode disposed on the substrate adjacent said insulator of said first device.

Cont'd A1

6. (Original) The programmable element of claim 5, wherein said third electrode comprises a diffusion region.

7. (Amended) ~~The programmable element of claim 6, wherein said first electrode comprises~~

A programmable element, comprising:

a first device on a substrate having a first electrode and a first insulator disposed between the substrate and said first electrode, said first insulator having a first value of a given parameter selected from the group consisting of density, thickness, and insulative value, that has a dielectric breakdown voltage that is less than that of a second insulator.

wherein said first device further comprises a second electrode having a diffusion

region disposed on the substrate adjacent said insulator of said first device, and th first electrode has a plurality of separate conductiv lines overlay the diffusion region;

a second device on a substrate having a third electrode and the second insulator disposed between the substrate and said third electrode, said second insulator having a second value of said given parameter that is different from said first value;

wherein said first and third electrodes of said first and second devices are coupled to one another; and

a source of programming energy coupled to said first device and causing it to permanently decrease in resistivity,

wherein a programmed state of said first device is indicated by a conductive state of said second device.

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8. (Original) The programmable element of claim 5, wherein said source of programming energy is coupled to said third electrode.

9. (Amended) The programmable element of claim 6, wherein said source of programming energy comprises a voltage source.

10. (Amended) ~~The programmable element of claim 2, wherein said first device comprises~~

A programmable elem nt, comprising:

a first device having a diode ~~and said second device comprises~~ on a substrate having a first ~~electrode~~ and a first insulator disposed between the substrate and said first electrode, said first insulator having a first value of a given parameter selected from the group consisting of density, thickness, and insulative value;

a second device having an FET on a substrate having a second electrode and a second insulator disposed between the substrate and said second electrode, said second insulator having a second value of said given parameter that is different from said first value;

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wherein said first and second electrodes of said first and second devices are coupled to one another; and

a source of programming energy coupled to said first device and causing it to permanently decrease in resistivity,

wherein a programmed state of said first device is indicated by a conductive state of said second device.

11. (Original) The programmable element of claim 10, further comprising a sense latch coupled to a controlled electrode of said FET.

12. (Original) The programmable element of claim 10, wherein said sense latch changes state when said diode is programmed.

13. (Amended) A programmabl l ment, comprising a programming device comprised of a first integrated circuit l ment having a first dielectric breakdown voltage and a MOSFET gain device comprised of a second integrated circuit element having a second dielectric breakdown voltage higher than said first dielectric breakdown voltage, said first and second integrated circuit elements each having at least one electrode, said electrodes being electrically coupled together and to a source of programming energy, said second integrated circuit element conducting current when the first integrated circuit element has been programmed.

Cont'd A1

14. (Original) The programmable element of claim 12, wherein said source of programming energy renders said first integrated circuit element permanently conductive when programmed, without rendering said second integrated circuit element permanently conductive.

15. (Amended) ~~The programmable element of claim 13, wherein~~ A programmable element, comprising a programming device comprised of a first integrated circuit element having a first dielectric breakdown voltage and a gain device comprised of a second integrated circuit element having a second dielectric breakdown voltage higher than said first dielectric breakdown voltage, said first and second integrated circuit elements each having at least one electrode, said electrodes being electrically coupled together and to a source of programming energy wherein said first integrated circuit element comprises a diffused electrode that is coupled to said source of programming nergy and said second integrated circuit element conducting current when the first

integrated circuit I ment has been programmed.

16. (Original) The programmable element of claim 14, wherein said first integrated circuit element comprises a conductive electrode comprised of a plurality of separate conductive lines disposed above said diffused electrode and separated therefrom by an insulator.

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17. (Original) The programmable element of claim 15, wherein said first integrated circuit element comprises a diode and said second integrated circuit element comprises an FET.

18. (Cancel) ~~A method of forming an integrated circuit including a programmable element, comprising the steps of:~~

~~forming a first device on a substrate having a first electrode and a first insulator disposed between the substrate and said first electrode, said first insulator having a first value of a given parameter;~~

~~forming a second device on a substrate having a second electrode and a second insulator disposed between the substrate and said second electrode, said second insulator having a second value of said given parameter that is different from said first value;~~

~~coupling said electrodes of said first and second devices to one another; and~~

~~coupling a source of programming energy to said first device.~~

19. (Cancel) ~~The method of claim 17, wherein said given parameter is selected from the group consisting of thickness, density, and insulative value.~~

20. (Cancel) ~~The method of claim 18, wherein said first device and said second device are formed by~~

~~forming a first dielectric on the substrate;~~

~~masking areas on the substrate where said first device is to be formed, so as to expose a portion of said first dielectric where said second device is to be formed;~~

~~treating said exposed portion of said first dielectric.~~

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21. (Cancel) ~~The method of claim 19, wherein said treatment step comprises forming a second dielectric on said exposed portion of said first dielectric.~~

22. (Cancel) ~~The method of claim 18, wherein said first device and said second device are formed by~~

~~forming a first dielectric on the substrate;~~

~~masking areas on the substrate where said second device is to be formed, so as to expose a a portion of said first dielectric where said second device is to be formed; and~~

~~treating said exposed portion of said first dielectric.~~

23. (Cancel) ~~The method of claim 21, wherein said treatment step is selected from the~~

~~group consisting of implantation of ions causing physical damage to said exposed portion of said first dielectric, implantation of ions altering conductivity characteristics of said exposed portion of said first dielectric, implantation of ions that both cause physical damage to and alter conductivity characteristics of said exposed portion of said first dielectic, and etching said exposed portion of said first dielectric.~~

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24. (Cancel) ~~The method of claim 17, wherein said electrode of said first device has conductivity characteristics that are different from those of said electrode of said second device.~~

25. (Cancel) ~~The method of claim 23, wherein both of said electrodes of said first and second devices are made of polysilicon, said electrode of said first device is implanted with ions at a first concentration, and said electrode of said second device is implanted with ions at a second concentration less than said first concentration.~~

26. (Cancel) ~~The method of claim 17, further comprising~~

~~forming a first dopant region below said first electrode; and~~

~~forming a second dopant region on either side of said second electrode.~~

27. (Cancel) ~~The method of claim 25, wherein said first dopant region has a dopant concentration that is greater than that of said second dopant region.~~

28. (Cancel) ~~The method of claim 25, wherein said first and second dopant regions~~

~~abut one another.~~

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29. (Cancel) ~~The method of claim 25, wherein said first and second depant regions are separated by an isolation space, and are interconnected by an overlaying conductor.~~